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The book seems intended to be used for a course on digital logic design. The first few chapters deal with digital logic design generally, and can be ignored. The discussion of Verilog begins after 100 pages in chapter 4. The core of the book is chapters 4 through 7, which describe how to define digital logic with Verilog.

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His areas of interest include Modeling, synthesis and verification of digital systems with hardware description languages, system-level design languages, and embedded systems with FPGAs. He is the author of Advanced Digital Design with the Verilog HDL and the co-author of Digital Design, 4e.

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